Publication List for MSLAB (1994-present)

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Books


Book Chapters:

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Ph.D. Thesis

D1. Sheldon X.-D. Tan, “Symbolic Analysis of Large Analog Circuits with Determinant
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Decision Diagrams”, University of Iowa, 1999.

Ph.D. Student Theses


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**Conference Presentations and Posters** (informal publication).


I4. The workshop presentation by Sheriff in ICCAD18.
Tutorials:


T3. Sheldon X.-D. Tan, “Advanced modeling and analysis techniques for nanometer interconnect and multi-core VLSI circuits”, ASIC & System Lab, Fudan University, July 27 to Aug. 1, 2008. (Four presentations were given in the short course), Shanghai, China.


Patents:


P4. X. Hong, Y, Cai, Z. Pan, Y. Luo, J. Fu, Sheldon X.-D. Tan, “Transient analysis of on-
chip power grid networks based on equivalent circuits”, Patent No. ZL 03104770.X (China)


P7. One more patent here 2D FEM analysis of PCB

Journal Articles


J45. D. Li, S. X.-D. Tan, “Statistical analysis of large on-chip power grid networks by
variational_reduction_scheme”


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J89. H. Zhao and S. X.-D. Tan, “Postvoiding FEM analysis for electromigration failure


Symposia/Conference Proceedings


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C87 D. Li, S. X.-D. Tan, G. Chen and X. Zeng, “Statistical analysis of on-chip power


C98 D. Li, S. X.-D. Tan, N. Mi and Y. Cai, “Efficient power grid integrity analysis using


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<th>Publication Number</th>
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<th>Title</th>
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<tr>
<td>C116</td>
<td>Z. Liu, S. X.-D. Tan, H. Wang, R. Quintanilla and A. Gupta</td>
<td>“Compact thermal modeling for package design with practical power maps”</td>
<td>1st International IEEE Workshop on Thermal Modeling and Management: Chips to Data Centers (TEM), Orlando</td>
<td>Jul. 2011</td>
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<tr>
<td>C117</td>
<td>Z. Liu, S. X.-D. Tan, H. Wang, R. Quintanilla and A. Gupta</td>
<td>“Compact behavioral thermal modeling for microprocessor design with spatially correlated power inputs”</td>
<td>TECHCON’2011, Austin</td>
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C159 K. He, X. Huang, S. X.-D. Tan, “EM-based on-chip aging sensor for detection and prevention of counterfeit and recycled ICs”, *Proc. IEEE/ACM International Conf. on Computer-Aided Design (ICCAD’15)*, Austin, TX, Nov. 2015. (invited)


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