COURSE SYLLABUS (Spring 2026)
EE100A Electronic Circuits I

Spring 2026
Dept. of Electrical and Computer Engineering
University of California, Riverside

Instructor:	Prof. Albert Wang	Office:	417 WCH
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Office Hours:	Tuesday 11am-1pm	Web:	http://www.ece.ucr.edu/~aw
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Course Description:	*Electronic systems, linear circuits, operational amplifiers, diodes, nonlinear circuit applications, junction and metal-oxide-semiconductor field effect transistors, bipolar junction transistors, MOS and bipolar digital circuits. Laboratory experiments are performed in the subject areas, and SPICE simulation is used.*
Lecture	**Tu/Th, 6:30pm-7:50pm**, Rm1000 Gordon Watkins Hall
Lab	W/11a-1:50pm; W/2-4:50pm; Th/9-11:50a; Th/12-2:20p; F3-5:50p; Room WCH121
Text: (required)	*Microelectronic Circuits*, Sedra & Smith, 7th Ed., 2014, Oxford, ISBN: ISBN-13: 978-0199339136 (*Any Editions OK*)
Exam:	Mid-term: 5/7; Final: Saturday, June 6, 11:30 a.m. - 2:30 p.m.
Grades:	Exam: 60% + HW: 10% + Labs: 30%

Topical Outlines & Schedule (Subject to modification upon progresses)

Weeks	Date	Lecture Contents		Labs
1	3/31&4/2		Junctions, diodes, metal-oxide-semiconductor (MOS) field effect transistors, bipolar junction transistors (BJT), linear circuits, nonlinear circuit, operational amplifiers, etc.	
2	4/7&9			
3	4/14&16			
4	4/21&23			Lab 0
5	4/28&30			Lab 1
6	5/5&7	Mid-Term 5/7		Lab 2
7	5/12&14			Lab 3
8	5/19&21			Lab 6
9	5/26&28			Lab 4
10	6/2&4			Lab 5
11	Final	6/6/Sat		