

**COURSE SYLLABUS (Spring 2024)**  
**EE100A Electronic Circuits I**

Spring 2024  
 Dept. of Electrical and Computer Engineering  
 University of California, Riverside

Instructor:	Prof. Albert Wang	Office:	417 WCH
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Course Description:	<i>Electronic systems, linear circuits, operational amplifiers, diodes, nonlinear circuit applications, junction and metal-oxide-semiconductor field effect transistors, bipolar junction transistors, MOS and bipolar digital circuits. Laboratory experiments are performed in the subject areas, and SPICE simulation is used.</i>
Lecture	Tu/Th, 6:30pm-7:50pm, BRNHL A125 (Bourns Hall)
Lab	Weds, 11a-1:50pm; 2-4:50pm; Thur., 9-11:50a; Room WCH121
Text: (required)	<i>Microelectronic Circuits</i> , Sedra & Smith, 7 <sup>th</sup> Ed., 2014, Oxford, ISBN: ISBN-13: 978-0199339136
Exam:	Mid-term: 5/7; Final: 6/13 TH
Grades:	Exam: 60% + HW: 10% + Labs: 30%

Topical Outlines & Schedule (Subject to modification upon progresses)

Weeks	Date	Lecture Contents	Labs	
1	4/2&4	Junctions, diodes, metal-oxide-semiconductor (MOS) field effect transistors, bipolar junction transistors (BJT), linear circuits, nonlinear circuit, operational amplifiers, etc.		
2	4/9&11		<b>Extra lectures in Weds/Lab</b>	<b>4/10W 11-1:50</b>
3	4/16&18			
4	4/23&25			Lab 0
5	4/30&5/2			Lab 1
6	5/7&9		<b>Mid-Term on 5/7</b>	Lab 2
7	5/14&16			Lab 3
8	5/21&23			Lab 6
9	5/28&30			Lab 4
10	6/4&6		<b>to 4/10 lab time zoom</b>	Lab 5
11	Final		6/13/TH, 7pm - 10pm	